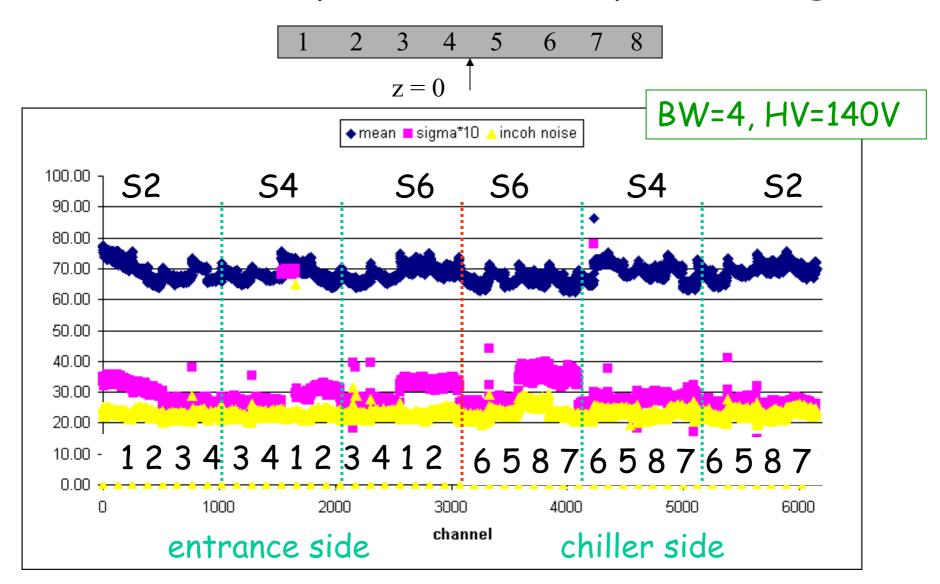
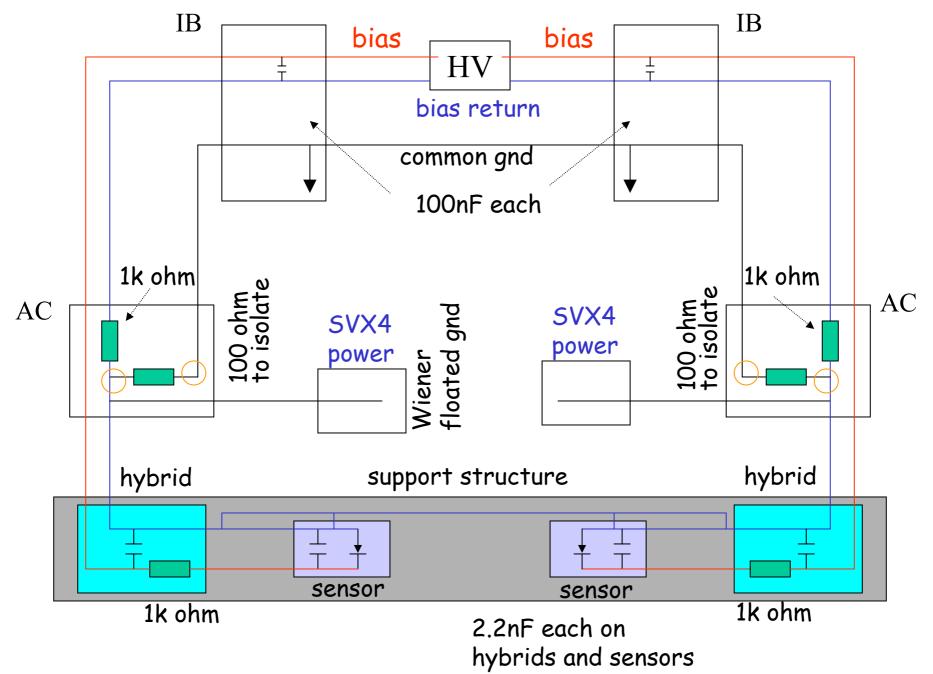
Position Dependence (A-layer testing)

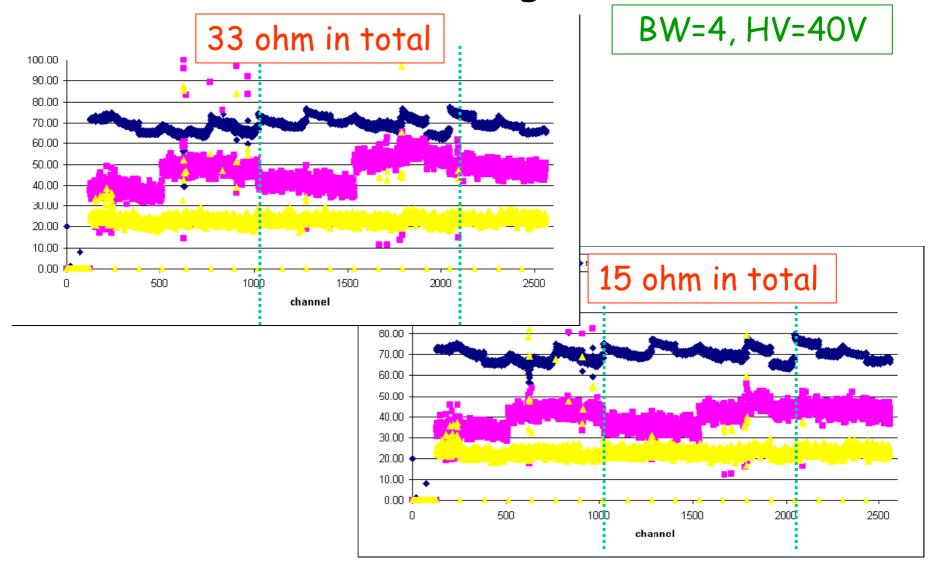




Testing at electrical support structure

- The read out system is back up
 - now we have BiRa HV system bias scheme is the same as the real system (we used Keithley before)
- Ron had found the mistake in bias jumper configuration in interface board → it was fixed
- Huge (pedestal moves around between 0 and 255...) coherent noise
 - differential noise OK (~2 ADC counts)
 - this was gone after removing all VCAL 0 ohm resistors

Resistor across isolated and non-isolated ground



Contributions from other IB (or power)?

